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Both Law (2007, p. 248-253) and Banks et al. (2005, p.356-357) list eight verification techniques which include debugging model components, using multiple reviewers, observing the simulated system states during a simulation run, making use of animation, writing good model documentation, etc.

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review the classical simulation modelling frameworks by Shannon (1998), Law and Kelton (2000), and Banks et al. (2001), shown in Figures 1, 2, and 3 respectively.

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